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GIG ADC Reference Boards & Design Tools

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National's Ultra High-Speed A/D Converters. The suffix "DEV" stands for "Development System" and the suffix "RB" stands for "Reference Board". Although the naming convention is different, both boards serve the same purpose.

COMPANY

The Ultra High Speed A/D converter reference boards are self-contained and operate with proprietary WaveVision software to set the operating parameters of the converter, then capture and analyze the data from the converter. The reference board includes an on-board power supply, sampling clock, and data capture so that the only external equipment required is the analog input source.

In order to facilitate development of system design, the schematics, layout, WaveVision software and IBIS Models are made available. The FPGA code can also be made available as-is for custom development under NDA.

8-bit Gigasample ADC Reference Boards

Speed	Product	Users' Guide	Design Package	Order Reference Board	Data Capture and Evaluation Software	IBIS Model
	➡ ADC08500	Gig Users Guide.pdf	ADC08(D)XXXX	ADC08500DEV	WaveVision4.3.24B	N/A
500 Msps	₫ ADC08D500	Gig Users Guide.pdf	ADC08(D)XXXX	ADC08D500DEV	WaveVision4.3.24B	adc08d500.ibs
	ADC08DL500	N/A	N/A	N/A	N/A	adc08dl500.ibs
	➡ ADC081000	N/A	N/A	Use the ADC08D1000DEV	N/A	adc081000.ibs
1.0 Gsps	₫ ADC08D1000	Gig Users Guide.pdf	ADC08(D)XXXX	ADC08D1000DEV	WaveVision4.3.24B	adc08d1000.ibs
	➡ ADC08D1020	Gig Users Guide.pdf	ADC08(D)XXXX	ADC08D1020DEV	WaveVision4.3.24B	N/A
	➡ ADC081500	N/A	N/A	Use the ADC08D1500DEV	N/A	adc081500.ibs
1.5 Gsps	₫ ADC08D1500	Gig Users Guide.pdf	ADC08(D)XXXX	ADC08D1500DEV	WaveVision4.3.24B	adc08d1500.ibs
	₫ ADC08D1520	Gig Users Guide.pdf	ADC08(D)XXXX	ADC08D1520DEV	WaveVision4.3.24B	adc08d1520.ibs
2.0.0	➡ ADC083000	ADC083000 RB_UG.pdf	ADC083000RB	ADC083000RB	WaveVision08x3000	adc083000.ibs
3.0 Gsps	➡ ADC08B3000	ADC083000 RB_UG.pdf	ADC08B3000RB	ADC08B3000RB	WaveVision08x3000	N/A

10-bit Gigasample ADC Reference Boards

Speed	Product	Users' Guide	Design Package	Order Reference Board	Data Capture and Evaluation Software	IBIS Model
1.0 Gsps	➡ ADC10D1000	User Guide	ADC10D1x00RB	ADC10D1000RB	WaveVision5	adc10d1x00.ibs
1.5 Gsps	➡ ADC10D1500	Users Guide	ADC10D1x00RB	ADC10D1500RB	WaveVision5	adc10d1x00.ibs

12-bit Gigasample ADC Reference Boards

Speed	Product	Users' Guide	Design Package	Order Reference Board	Data Capture and Evaluation Software	IBIS Model
1.0 Gsps	➡ ADC12D1000	N/A	N/A	Use the ADC12D1600	N/A	adc12d1x00.ibs
1.6 Gsps	➡ ADC12D1600	User Guide	ADC12D1x00RB	ADC12D1600	WaveVision5	adc12d1x00.ibs
1.8 Gsps	➡ ADC12D1800	User Guide	ADC12D1x00RB	ADC12D1800	WaveVision5	adc12d1x00.ibs

12-bit Gigasample RF-sampling ADC Reference Boards

Speed	Product	Users' Guide	Design Package	Order Reference Board	Data Capture and EvaluationSoftware	IBIS Model
500 Msps	ADC12D500RF	N/A	N/A	Use the ADC12D800RFRB	N/A	N/A
800 Msps	➡ ADC12D800RF	Users' Guide	ADC12D800RFRB	ADC12D800RFRB	WaveVision5	N/A
1.0 Gsps	ADC12D1000RF	N/A	N/A	Use the ADC12D1600RFRB	N/A	N/A
1.6 Gsps	ADC12D1600RF	Users' Guide	ADC12D1x00RFRB	ADC12D1600RFRB	WaveVision5	N/A
1.8 Gsps	ADC12D1800RF	Users' Guide	ADC12D1x00RFRB	ADC12D1800RFRB	WaveVision5	N/A

Note: "N/A" means "Not Available". The Design Package includes the following: software license agreement, schematic and bill of materials, layout, and FPGA source code.

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